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CARY Y. YANG received the B.S., M.S., and Ph.D. degrees in electrical engineering from the University of Pennsylvania in 1970, 1971, and 1975, respectively. For his doctoral research, he studied the electronic and optical properties of IV-VI narrow-gap semiconductors. His postdoctoral work at M.I.T. introduced him to the field of surface science, where he examined the detailed electronic structure of chemisorbed molecules on heavy transition metal surfaces.

He joined NASA Ames Research Center in Moffett Field, California in 1976 and extended his chemisorption study to include surfaces of submicron metal particles. Working with theoretical chemists as well as electron microscopists at Ames, he was able to model and verify the five-fold (hence non-bulk) symmetry of these particles. After a brief stay at Stanford University in the Stanford-NASA Ames Joint Institute for Surface and Microstructure Research, he founded Surface Analytic Research, Inc. in Mountain View, California, and directed sponsored research in surface and nanostructure science. In 1983 he joined Santa Clara University and founded the Microelectronics Laboratory, for teaching and research on silicon-based devices and circuits. He served as Associate Dean of Engineering from 2003 to 2006. He currently holds the positions of Professor of Electrical Engineering and Director of Center for Nanostructures. His current research is on nanostructure interfaces and interconnects in electronic and biological systems.

Over the past two decades, Professor Yang has initiated innovative programs to educate and train technical professionals in various stages of their careers. In the eighties, he developed and organized short courses on timely topics in silicon technology to Silicon Valley professionals. In the mid-nineties, he offered short courses on semiconductor technology for SEMI as part of a retraining program for professionals in other fields. Since the mid-eighties, he has provided opportunities for his students to spend extended periods in companies in Japan, where they collaborated with their hosts on their thesis research. More recently, he founded the Center for Nanostructures at Santa Clara, which offers interdisciplinary research and education opportunities in the field of nanoscience and nanotechnology for university students and faculty, high school students and teachers, as well as Silicon Valley technical professionals.

Dr. Yang has been a consultant to industry and government, and a visiting professor at Tokyo Institute of Technology, University of Tsukuba, National University of Singapore, University of Pennsylvania, University of California, San Diego, and University of California, Berkeley. He is a Fellow of IEEE and served as Santa Clara Valley Chapter Chair, Regions/Chapters Chair, Vice President, and President of the IEEE Electron Devices Society. In 2001, on behalf of People to People Ambassadors Program, he led an Electron Devices Delegation to visit universities, government institutes, and companies in the People's Republic of China. From 2002 to 2003, he served as an elected member of the IEEE Board of Directors, representing Division I. He was an editor of the IEEE Transactions on Electron Devices, in the area of MOS devices. In 2004, he was named the recipient of the IEEE Educational Activities Board Meritorious Achievement Award in Continuing Education "for extensive and innovative contributions to the continuing education of working professionals in the field of micro/nanoelectronics". In 2005, he was honored with the IEEE Electron Devices Society Distinguished Service Award.

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