

Journal Publications

Q. Ngo, A.M. Cassell, V. Radmilovic, J. Li, S. Krishnan, M. Meyyappan, and C.Y. Yang, "Palladium catalyzed formation of carbon nanofibers by plasma enhanced chemical vapor deposition," *Carbon* 45, 424-428 (2007).

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S. Yu, D.M. Petranovic, S. Krishnan, K. Lee, and C.Y. Yang, "Loop-Based Inductance Extraction and Modeling for Multiconductor On-Chip Interconnects," *IEEE Transactions on Electron Devices* 53, 135-145 (2006).

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F. Madriz, J. R. Jameson, S. Krishnan, K. Gleason. X. Sun, and C.Yang, "Measurement and Circuit Model of Carbon Nanofibers at Microwave Frequencies," *Proceedings of 2008 IEEE International Interconnect Technology Conference*, Burlingame, California, June 1-4, 2008, pp. 138- 140.

K. Ring, and S. Krishnan, " Long-term Jitter Reduction through supply noise compensation," Proceedings of the IEEE International Symposium on Circuits and Systems, (ISCAS) May 2008.

S. Raman, S. Krishnan and A. Fiedler, "A precise clock phase multiplier," Proceedings of the IEEE International Symposium on Circuits and Systems, (ISCAS) May 2005.

Q. Ngo, A. M. Cassell, J. Li, S. Krishnan, M. Meyyappan, and C. Y. Yang, "Vertically aligned carbon nanofiber arrays for on-chip interconnect applications", Proceedings of 2005 IEEE International Interconnect Technology Conference, pp 153-155.

Q. Ngo, S. Krishnan, A. M. Cassell, Y. Ominami, J. Li, M. Meyyappan, and C. Y. Yang, "Electrical characterization of carbon nanofibers for on-chip interconnect applications", Proceedings of IEEE NANO, pp 585-588, 2005. (Invited)

Q. Ngo, Y. Ominami, H. Yoong, A. J. Austin, A. M. Cassell, B. A. Cruden, J. Li, S. Krishnan, M. Meyyappan, and C. Y. Yang, "Electrical and structural characterization of vertically aligned carbon nanofibers synthesized by plasma-enhanced CVD", HP QSR Symposium, March 2005.

S. Yu, S. Sim, S. Krishnan, D.M. Petranovic, K. Lee, and C.Y. Yang, "Unified Model for On-Chip Interconnects," Proceedings of the 7th International Conference on Semiconductor and Integrated Circuit Technology," Beijing, October 18-21, 2004, Paper #D3.1. (Invited paper)

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F. Madriz, J.R. Jameson, S. Krishnan, K. Gleason, X. Sun, and C.Y. Yang, "Measurement and circuit model of carbon nanofibers at radio frequencies," Materials Research Society Spring Meeting, Symposium P: Carbon Nanotubes and Related Low-dimensional Materials, San Francisco (March 2008).

X. Sun, F. Madriz, W. Wu, J.R. Jameson, S. Krishnan, and C.Y. Yang, "Test structure for measurement of high-frequency behavior of one-dimensional on-chip interconnect materials," Materials Research Society Spring Meeting, Symposium N: Materials and Processes for Advanced Interconnects for Microelectronics, San Francisco (March 2008).

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Venkatraman, V., S. Krishnan and N. Ling, "Architecture for de-blocking filter in H.264," Proceedings of the Picture Coding Symposium 2004 (PCS), December 2004.

S. Yu, S. Krishnan, and C.Y. Yang, "Interconnect Modeling for Frequency-Dependent Crosstalk Noise Analysis," Symposium on Compact Modeling, IEEE Electron Device Society Santa Clara Valley Chapter, May 7, 2004. (invited presentation)

Conference presentations accepted

W. Wu, X. Sun, J. Jameson, D. Nguyen, P. Wilhite, S. Krishnan, and C. Y. Yang, "Electrical Properties of Carbon Nanofiber Interconnect Vias", Materials Research Society (MRS) 2008 Fall Meeting, December 2008.

Conference papers accepted

K. Abugharbieh, J. Mohan, D. Varadarajan, I. Duzevik and S. Krishnan, " An ultra low power 10 Gbps LVDS output driver," accepted for the 2008 IEEE Bipolar / BiCMOS Circuits and Technology Meeting (BCTM) Monterey,CA.

K. Abugharbieh, S. Krishnan, J. Mohan, and D. Varadarajan, " A Low Power 10 Gbps Voltage Mode Output Driver with Good Return Loss Performance," accepted for the 2008 International Conference on Microelectronics (ICM), Sharjah.