



DEPARTMENT OF ELECTRICAL ENGINEERING

ASIC DESIGN AND TEST

CERTIFICATE CHECKLIST

School of Engineering

STUDENT NAME _____ SCU ID# _____

REQUIRED COURSES (14 UNITS)

Course #	Title	Term Taken	Grade	Units	Units Taken
ELEN 387	VLSI Design I			2	
ELEN 500	Logic Analysis and Synthesis			2	
ELEN 603	Logic Design Using HDL			2	
ELEN 605	High-Level Synthesis			2	
ELEN 608	Design for Testability			2	
ELEN 613	Formal Verification of Digital Hardware			2	
ELEN 624	Signal Integrity in IC and PCB Systems			2	

ELECTIVE COURSES (select any 2 units)

ELEN 388	VLSI Design II			2	
ELEN 389	VLSI Physical Design			4	
ELEN 604	Semicustom Design with Programmable Devices			2	
ELEN 615	Formal Hardware Verification III			2	
ELEN 620	Design of System on a Chip			2	
TOTAL					

G.P.A.GRE test requirement for admission to the MSEE degree program

GRE required

GRE waived

Chair's Signature

Date